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### Error detection for fiber distributed interfaced optic link

SR Chandran, FE Mau, SA Jan - US Patent 5,068,854, 1991 - Google Patents

... The input bus 32 communicates the information packet to a ... 62 also will assert the **error flag signal** ... information packet, together with the **check code** generated ...

Cited by 8 - Related articles - All 6 versions

### Method and system for error correction over serial link

MD Ornes, CIW Norris, GK Chui - US Patent App. 10/835,711, 2004 - Google Patents

... of the ECC code is checked by **checking** the parity of ... does not match the selected **parity**, an **error** in the ... 602 XOR the appropriate bits of the **input** character in ...

Related articles - All 4 versions

### Method and structure for securing access to a computer system

BW Watson, RJ Lee - US Patent 5,475,839, 1995 - Google Patents

... the system from a known floppy disk and **checking** the hard ... If a potential **error** is detected, system boot is halted ... which the user has not provided **input** to the ...

Cited by 31 - Related articles - All 2 versions

### [book] Error coding for engineers

A Houghton, 2001 - books.google.com

... message will be more than t from any **valid** code so ... the Golay code, you find that 15 **check** bits should ... one form or another, is what makes **error** coding possible. ...

Cited by 3 - Related articles - BL Direct

### Method and structure for providing computer security and virus prevention.

BW Watson, RJ LEE, 1991 - freepatentsonline.com

... Other **input/output** functions such as data communication ... **Error checking** and correction are provided through firmware ... The **checking** and correction processes are ...

All 3 versions

### Arrangement and method of downloading data to a plurality of destinations in a digital ...

MW Pflueger, MS Dailey, DA Slobodianuk, DL ... - US Patent 5,201,045, 1993 - Google Patents

... MOVE DATE TO MEMORY GET EOF MSG (LOAD CRC, \* OF BLOCKS ... the signals the message into the DFTC's **input** buffer, then ... when the DFTC sends a CLOCK-GENERATOR 417 also ...

Cited by 6 - Related articles - All 2 versions

### Data protocol

SL Pope, D Roberts, D Riddoch, D Clarke - US Patent App. 10/548,126, 2004 - Google Patents

... in this example a PCI (**Input/Output** bus subsystem ... polynomials may be the USB **CRC-16** polynomial. ... and if the received data represents **error check** data verifying ...

All 3 versions

### [PDF] ► Error Behaviour in Optical Networks

LB James - michaelandlaura.org.uk

... 4.2 Frame validity **check** process at the PCS and ... **error** detection and/or correction mechanisms are to ... speed, so deserialisation at the **input**, and serialisation ...

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[PDF] ► **Clean Factory Automation IPT System**

S Ly - [ece.auckland.ac.nz](mailto:ece.auckland.ac.nz)

... **7 errors** • Has a high probability of detecting burst **error** greater than ... then attached to the end of the **input** data and ... The **CRC check** is also performed the on

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**Peer to peer network for a mobile radio transceiver**

TE Dailey - US Patent 5,642,350, 1997 - [Google Patents](#)

... YES 220 NO **CRC OK?** ... timing **generator** 179 running many times faster than the The UART is an ... in the art, a differential bus transceiver uses the **input** to the ...

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